**Annual Progress Report - I**

on

# ABCEFGHIJKLMNOPQRSTUVWXYZ

Submitted in partial fulfillment of the requirement
for the award of the Degree of

**Doctor of Philosophy**

in

**Computer Applications**

by

**XYZ**

under the guidance of

**ABC**

**Computer Applications**
Bharati Vidyapeeth's
Institute of Management and Information Technology
CBD Belapur, Navi Mumbai -400614
University of Mumbai
June 2017

**Faculty: Technology( Computer Applications)**

**Annual Progress Seminar-I**

**Area: ————————————————————-**

**Topic of Thesis: ————————————————-**

**Date of Admission: ¡date to be Mentioned**

**Name of Research Centre: Bharati Vidyapeeth's Institute of Management Information Technology, CBD Belpaur Navi Mumbai 400614**

**Name of Student: ————————————————**

**Name of Supervisor: ————————————————**

**Signature of Student**

**Signature of Supervisor**

**Signature of Dean R&D** **Signature of Principal**

**¡Name of Guide¿** **Dr. Suhasini Vijaykumar**

## Statement by the Candidate

**I wish to state that the work embodied in this synopsis titled "Electrical Load Emulation using Hardware in the Loop" forms my own contribution to the work carried out under the guidance of Dr. XYZ at the Bharti Vidyapeeth's Institute of Management and Information Technology. I declare that this written submission represents my ideas in my own words and where others' ideas or words have been included, I have adequately cited and referenced the original sources. I also declare that I have adhered to all principles of academic honesty and integrity and have not misrepresented or fabricated or falsified any idea/data/fact/source in my submission.**

**(Candidate Signature)**

**Name:**

**Roll Number:**

## Abstract

## Objectives of the Research

## Previous APS Comments

## Current Progress of Research

## Discussion/Summary

## Further Research work

# References


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