

Ph.D. Title Proposal
on
ABCEFGHIJKLMNOPQRSTUVWXYZ

Submitted in partial fulfillment of the requirement
for the award of the Degree of

Doctor of Philosophy
in
Computer Applications

by

XYZ

under the guidance of

ABC



Computer Applications
Bharati Vidyapeeth's
Institute of Management and Information Technology
CBD Belapur, Navi Mumbai -400614
University of Mumbai
June 2017

Faculty: Technology(Computer Applications)

Area: _____

Topic of Thesis: _____

Date of Admission: ;date to be Mentioned

Name of Research Centre: Bharati Vidyapeeth's Institute of Management Information Technology, CBD Belpaur Navi Mumbai 400614

Name of Student: _____

Name of Supervisor: _____

Signature of Student

Signature of Supervisor

Signature of Dean R&D

;Name of Guide;

Signature of Principal

Dr. Suhasini Vijaykumar

Statement by the Candidate

I wish to state that the work embodied in this synopsis titled “Electrical Load Emulation using Hardware in the Loop” forms my own contribution to the work carried out under the guidance of Dr. XYZ at the Bharti Vidyapeeth’s Institute of Management and Information Technology. I declare that this written submission represents my ideas in my own words and where others’ ideas or words have been included, I have adequately cited and referenced the original sources. I also declare that I have adhered to all principles of academic honesty and integrity and have not misrepresented or fabricated or falsified any idea/data/fact/source in my submission.

(Candidate Signature)

Name:

Roll Number:

Motivation

Objectives

Literature Survey

Methodology

Outcomes of the Research

References

- [1] Y. Srinivasa Rao and M. C. Chandorkar ‘Numerical Integration Methods for Digital Power Electronics and Electrical Drives’, *Proceedings of the National Conference on Computational Intelligence for Electrical Engineering*, Sant Longowal Institute of Engg. and Tech., Punjab, India, pp. 20-24, 18-19 Nov. 2005.
- [2] Y. Srinivasa Rao and Mukul Chandorkar, ‘Rapid Prototyping Tool for Electrical Load Emulation using Power Electronic Converters’, under review for *Proceedings of the IEEE Symposium on Industrial Electronics and Applications*, Kuala Lumpur, Malaysia, 4-6 Oct. 2009.
- [3] Y. Srinivasa Rao and M. C. Chandorkar, ‘Load Emulation with Power Electronic Converters’, *Proceedings of the National Power Electronic Conference*, Indian Institute of Science, Bangalore, India, 17-19 Dec. 2007.
- [4] Y. Srinivasa Rao and Mukul Chandorkar, ‘Electrical Load Emulation using Optimal Feedback Control Technique’, *Proceedings of the IEEE International Conference on Industrial Technology*, Gippsland, Australia, 9-13 Feb. 2009.
- [5] R. R. Sawant and M. C. Chandorkar, ‘A Multifunctional Four-leg Grid Connected Compensator,’ *IEEE Transactions on Industry Applications*, vol. 45, no. 01, pp. 249-259, Jan/Feb 2009.